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APPLICANT(S) Sirichotiyakul et al. GROUP ART UNIT: 2123  
APPLN. NO.: 09/580,854 EXAMINER: Thangavclu, Kandasamy  
FILED: 30 May 2000  
TITLE: METHODS FOR ANALYZING INTEGRATED CIRCUITS AND  
APPARATUS THEREFOR

Certificate of Transmission under 37 CFR 1.8

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transmitted to the Patent and Trademark Office.

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AMENDMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Responsive to the Office Action dated 19 November 2004, and Examiner's comments with regard thereto, please enter the following amendments in the above-entitled application, without prejudice or disclaimer. No extension of time is believed to be necessary. However, if an extension is required, the extension of time is requested and the Commissioner is hereby authorized to charge Deposit Account 503079, Freescale Semiconductor, Inc., for fees associated with the extension.

If Applicant has overlooked any additional fees, or if any overpayment has been made, the Commissioner is hereby authorized to credit or debit Deposit Account 503079, Freescale Semiconductor, Inc.